

App. No. 10/501,791
Reply dated August 29, 2005 to
"Notice of Non-Compliant Amendment"

Please delete the pending abstract in its entirety, and replace it with the following abstract of the disclosure:

ABSTRACT

For semiconductor manufacturing equipment a ceramic susceptor is made available in which by optimizing the inter-wiring-line separation in the resistive heating element, damage due to shorting between resistive heating element lines during heating operations is prevented while wafer-surface temperature uniformity is maintained. The ceramic susceptor (1) for semiconductor manufacturing equipment has a resistive heating element (3a) on a surface of or inside ceramic substrate (2), with the smallest angle θ formed by the bottom and lateral sides of the resistive heating element (3a) in a section of the resistive heating element (3a) being 5° or greater. A plasma electrode may be arranged on a surface of or inside the ceramic substrates (2a) of the ceramic susceptor (1). The ceramic substrates (2a) are preferably made of at least one selected from aluminum nitride, silicon nitride, aluminum oxynitride, and silicon carbide.